

REMARKS

Reconsideration is respectfully requested.

Claims 1-10 are pending in this application. New claims 11-13 are added herein.

The Examiner indicated that claims 5, 7 and 9 would be allowable if rewritten to independent form, and also to attend to the 35 U.S.C. §112, second paragraph, issue raised by the Examiner. To respond to this point, applicants present new claims 11, 12 and 13 that correspond to claims 5, 7 and 9, rewritten to independent form, also with attention to the Examiner's formal matter concerns. These claims are respectfully submitted to be allowable.

The Examiner has objected to the title of the invention alleging it is not descriptive. Applicants provide a new title herein for the Examiner's consideration.

The Examiner has rejected claims 1-10 under 35 U.S.C. §112, second paragraph, alleging they are indefinite and fail to particularly point out and distinctly claim the subject matter regarded as the invention. Applicants respectfully traverse. It is respectfully submitted that the claim amendments as presented here overcome the Examiner's rejection on this point.

The Examiner has also rejected claims 1-4, 6, 8, and 10 under U.S.C. §102(b) as being anticipated by JP 7-336138. The specification describes the invention on page 2 lines 2-4 as "two conductors...mutually connecting both odd ends of them". Later, on page 3 line 8-10 the specification describes "when two conductors

are made in spiral shapes, it is preferable to connect an inner end of one conductor with an outer end of another conductor". In the "Best Mode" section and in the drawings, one end of one conductor being connected to one end of the other is the only depiction of interconnectivity. In particular with the spiral conductors shown in Figure 3, the only interconnectivity shown is the inner end of the top coil connected to the outer end of the bottom coil. Note the reference JP-07-336138 appears to disclose two conductors 322 and 324 in Fig 77 & 78(I) formed in piles on a substrate 320 being mutually insulated 326. Paragraph [0247] of JP-07-336138 describes how the details of the interconnectivity of the Fig 78/79 embodiment is shown in Fig 1. Fig 1 shows a coil between junction points 26 and 24 and another coil connected to the first coil at junction point 24. The second coil also interacts with the first as one half of a capacitive element. Fig 78(I) shows the coils are connected at the outside of each coil with 328. Also figure 2, of JP-07-336138, shows the coils are to be connected at one end only at the outside of the coil.

Claim 1 is amended to include "one end of one conductor is connected to the opposite end of the other conductor". JP-07-336138 does not anticipate nor teach or suggest providing an LC oscillator having two conductors in piles on a substrate and connecting one end of the top conductor with the opposite end of the bottom conductor with only the top conductor acting as an inductor. Accordingly claims 1-10 are submitted to be allowable.

Applicant also wishes to discuss JP 60-136156, which was cited by applicant recently. Referring to Fig 4 and 5. JP 60-

136156 shows two coils connected in series wound in the same direction but with the current flowing in opposite directions. Therefore, the magnetic field of one coil is directed opposite the other. JP 60-136156 discloses no strictly capacitive device. One skilled in the art would not be motivated to look to the JP 60-136156 reference nor be aided in any way in an attempt to make a better LC oscillator, as the applicant has. Furthermore, it is not clear how the arrangement shown would reduce eddy currents in the top layer as the applicant's arrangement does.

With respect to European publication EP 0661805, combining this with JP-07-336138 would not arrive at the subject matter of applicants' claims. The EP 0661805 reference does not provide any suggestion that would enable one skilled in the art to turn the device disclosed in JP-07-336138 into applicants' invention. While there is suggestion of shapes, a curved shape and an elongated shape, the interconnectivity shortcomings, and therefore the functionality shortcomings, of JP-07-336138 are not overcome by the teachings of EP 0661805.

EP 0661805 discloses a single inductor electrode formed on the surface of a semiconductor substrate positioned on a pn junction. The pn junction functions as a capacitor only when used in the reversed biased stated. Applicants include in claims 5 and 7 the limitation of 2 conductors.

None of the references cited, alone or in combination, teach or suggest providing an LC oscillator having two conductors in piles on a substrate and connecting one end of the top conductor

with the opposite end of the bottom conductor with only the top conductor acting as an inductor.

Claim 1 as amended is believed to avoid the Examiner's rejections and is believed to be in condition for allowance. Claims 2-10 which depend from and include all the limitations of claim 1 are also believed to be in condition for allowance.

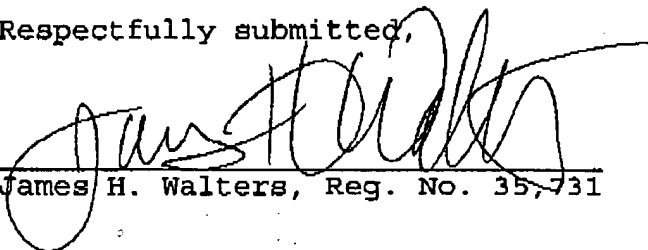
In view of the above amendments and remarks, claims 1-13 are believed in condition for allowance.

The specification is amended to correct a typographical error, where the word "emitter" was employed instead of the word "base". It is apparent from inspection of FIG. 1, the capacitor 26 is between the base and collector of the transistor 20, not between the emitter and collector thereof. Therefore, this is not new matter.

No amendment made was related to the statutory requirements of patentability unless expressly stated herein. No amendment made was for the purpose of narrowing the scope of any claim, unless applicants have argued herein that such amendment was made to distinguish over a particular reference or combination of references.

In light of the above noted amendments and remarks, this application is believed in condition for allowance and notice thereof is respectfully solicited. The Examiner is asked to contact applicants' attorney at 503-224-0115 if there are any questions.

Respectfully submitted,

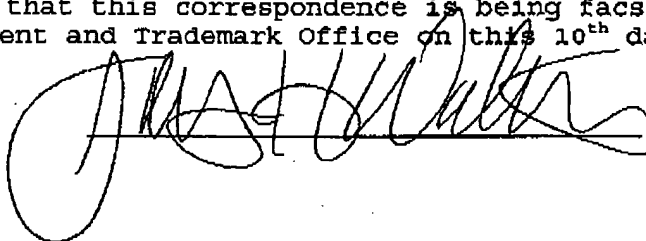


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